

GECCO 2013 Tutorial: Cartesian Genetic Programming



Julian F. Miller Dept of Electronics University of York, UK julian.miller@york.ac.uk



Copyright is held by the author/owner(s). GECCO'13 Companion, July 6–10, 2013, Amsterdam, The Netherlands. ACM 978-1-4503-1964-5/13/07.

Abstract

2 13

- Cartesian Genetic Programming (CGP) is an increasingly popular and efficient form of Genetic Programming. Cartesian Genetic Programming is a highly cited technique that was developed by Julian Miller in 1999 and 2000 from some earlier joint work of Julian Miller with Peter Thomson in 1997.
- In its classic form, it uses a very simple integer based genetic representation of a program in the form of a directed graph. Graphs are very useful program representations and can be applied to many domains (e.g. electronic circuits, neural networks). In a number of studies, CGP has been shown to be comparatively efficient to other GP techniques. It is also very simple to program.
- Since then, the classical form of CGP has been developed made more efficient in various ways. Notably by including automatically defined functions (modular CGP) and self-modification operators (self-modifying CGP). SMCGP was developed by Julian Miller, Simon Harding and Wolfgang Banzhaf. It uses functions that cause the evolved programs to change themselves as a function of time. Using this technique it is possible to find general solutions to classes of problems and mathematical algorithms (e.g. arbitrary parity, n-bit binary addition, sequences that provably compute pi and e to arbitrary precision, and so on).

This tutorial is will cover the basic technique, advanced developments and applications to a variety of problem domains. The first edited book on CGP was published by Springer in September 2011. CGP has its own dedicated website http://www.cartesiangp.co.uk

Contents

- Classic CGP
- Modular CGP
- Self-modifying CGP
- Developmental CGP
- Cyclic CGP
- Applications
- Resources
- Bibliography

Genetic Programming

- The automatic evolution of computer programs
 - Tree-based, Koza 1992
 - Stack-based, Perkis 1994, Spector 1996 onwards (push-pop GP)
 - Linear GP, Nordin and Banzhaf 1996
 - Cartesian GP, Miller 1997
 - Parallel Distributed GP, Poli 1996
 - Grammatical Evolution, Ryan 1998



Origins of Cartesian Genetic Programming (CGP)

- Grew out of work in the evolution of digital circuits, Miller and Thomson 1997. First actual mention of the term *Cartesian Genetic Programming* appeared at GECCO in 1999.
- Originally, represents programs or circuits as a two dimensional grid of program primitives.
- This is loosely inspired by the architecture of digital circuits called FPGAs (field programmable gate arrays)

What defines CGP?

- The genotype is a list of integers (and possibly parameters) that represent the program primitives and how they are connected together
 - CGP represents programs as *graphs* in which there are *non-coding genes*
- The genes are
 - Addresses in data (connection genes)
 - · Addresses in a look up table of functions
 - Additional parameters
- This representation is very simple, flexible and convenient for many problems



















The CGP genotype-phenotype map

- When you decode a CGP genotype many nodes and their genes can be ignored because they are not referenced in the path from inputs to outputs
- These genes can be altered and make no difference to the *phenotype*, they are non-coding
- Clearly there is a many-to-one genotype to phenotype map
- How redundant is the mapping?





How many genotypes of length *n* map to a phenotypes of length *k*?

n			k							
	1	2	3	4	5	6	7	8	9	
1	1									
2	1	1								
3	2	3	1							
4	6	11	6	1						
5	24	50	35	10	1					
6	120	274	225	85	15	1				
7	720	1764	1624	735	175	21	1			
8	5040	13068	13132	6759	1960	322	28	1		
9	40320	109584	118124	67284	22449	4536	546	36	1	

Average number of active nodes in a genotype of length 9 is 2.83

Clearly, with say a genotype of 100 nodes, the number of genotypes that map to a phenotype with say about 10 nodes is an astronomical number





Point mutation Most CGP implementations only use mutation. Carrying out mutation is very simple. It consists of the following steps. The genes must be chosen to be valid alleles // Decide how many genes to change: num_mutations // Decide how many genes to change: num_mutations // Decide how many genes to change: num_mutations // decide many genes to change // decide many genes // decide many genes to change // decide many genes // decide many genes







Crossover or not?

- Recombination doesn't seem to add anything (Miller 1999, "An empirical study...")
- However if there are multiple chromosomes with independent fitness assessment then it helps a LOT (*Walker, Miller, Cavill 2006, Walker, Völk, Smith, Miller, 2009*)
- Some work using a floating point representation of CGP has suggested that crossover might be useful (*Clegg, Walker, Miller 2007*)











- A number of studies have been carried out to indicate the importance to neutral search
 - Miller and Thomson 2000, Vassilev and Miller 2000, Yu and Miller 2001, Miller and Smith 2006)

Neutral search and the three bit multiplier problem (Vassilev and Miller 2000) Importance of neutral search can be demonstrated by eutral mutations Of looking at the success rate in evolving a correct three-bit 0.9 digital parallel multiplier Neutral mutations OFF circuit. 0.9 Graph shows final fitness obtained in each of 100 runs of 0.8 10 20 30 40 50 60 Evolutionary run 70 80 90 10 million generations with neutral mutations enabled compared with disabled neutral mutations.







Modular/Embedded CGP (Walker, Miller 2004, 2008)

- So far have described a form of CGP (classic) that does not have an equivalent of Automatically Defined Functions (ADFs)
- Modular CGP allows the use of modules (ADFs)
 - Modules are dynamically created and destroyed
 - Modules can be evolved
 - Modules can be re-used













Module Survival

- Twice the probability of a module being destroyed than created
- Modules have to replicate to improve their chance of survival
 - Lower probability of being removed
- Modules must also be associated with a high fitness genotype in order to survive
 - Offspring inherit the modules of the fittest parent



















Self-modifying Cartesian Genetic programming

- *A developmental form of CGP
 - Includes self modification functions in addition to computational functions
 - 'General purpose' GP system
 - Phenotype can vary over time (with iteration)
 - Can switch off its own self-modification
- Some representational changes from classic CGP...







Changes to CGP: Arguments

- Nodes also contain a number of 'arguments'.
 - 3 floating point numbers
 - Used in various self-modification instructions
 - Cast to integers when required





SMCGP: Functions

Two types of functions:

- Computational
 - Usual GP computational functions
- Self-modifying
 - Passive computational role (see later)



Some Self-Modification Functions Operator MOVE Start, End, Insert Moves each of the nodes between Start and End into the position specified by Insert DUP Start, End, Insert Inserts copies of the nodes between Start and End into the position specified by Insert DELETE Start, End Deletes the nodes between Start and End indexes CHF Node, New Function Changes the function of a specified node to the specified function Node, Connection1, Changes the connections in the CHC Connection2 specified node

SMCGP Execution Important first step: Genotype is duplicated to phenotype. Phenotypes are executed: Self modifications are only made to the phenotype.

Self Modification Process: The To Do list

- Programs are iterated.
- If triggered, self modification instruction is added to a To Do list.
- At the end of each iteration, the instructions on this list are processed.
- The maximum size of the To Do list can be predetermined

Publications using SMCGP

- ♦ General Parity Problem (CEC 2009)
- * Mathematical Problems (EuroGP 2009, GECCO 2007)
- ♦ Learning to Learn (GECCO 2009)
- Generating Arbitrary Sequences (GECCO 2007)
- Computing the mathematical constants pi and e (GECCO 2010)
- Ceneral adder and many other problems (GPEM Tenth Anniversary Special Issue, 2010)

Authors: Harding, Miller, Banzhaf

Computation of a SM node

- Functions can be appended to the To Do list under a variety of conditions
 - If active
 - If value(first input) > value(the second input.
- *And:
 - The To Do list isn't too big.



Evolving Parity

- Each iteration of program should produce the next parity circuit.
 - On the first iteration the program has to solve 2 bit parity. On the next iteration, 3 bit ... up to 22 parity
 - Fitness is the cumulative sum of incorrect bits
- ♦ Aim to find *general* solution
 - Solutions can be proved to general
 - See GPEM 2010 paper
- CGP or GP cannot solve this problem as they have a finite set of inputs (terminals)











Two dimensional SMCGP (SMCGP2)

Harding, Miller and Banzhaf 2011

♦ SMCGP2: genes

- FunctionConnections
- Numeric Constant
- Arguments are now 2 D vectors
 - SM size (SMS)
- SM location (SML)



SMCGP2: Vector relative addressing and Empty nodes

- There are empty nodes are represented by X
- The relative address from C to B is (2, 1)
- meaning 2 nodes to the left, and one node up.
- The relative address of C to A is (4,1).
- Note how the empty nodes are not counted when computing how many nodes back to connect.

Simplified SM function set Duplicate section, insert elsewhere. Duplicate section, overwrite elsewhere.

12

- Crop to a section.
- Delete a section.
- Add a row or column.
- Delete a row or column.

SMCGP2: Self Modifying Functions

• NULL





SMCGP2 versus SMCGP: Results

Parity

- Two functions sets used:
 - FULL: All 2-input Boolean functions used
 - REDUCED: only AND, OR, NAND, NOR used
- SMCGP2 solves general parity 6.3 times faster than SMCGP using the FULL functions set but is slower for the REDUCED function set

♦ N bit binary adder

• SMCGP2 solves it approximately 6 times faster than SMCGP

SMCGP:Some observations

✤In SMCGP there are implicit

- Loops
- Recursion
- Modules/functions
- Halting (telomeres)
- Also have "partial" loops/recursion



Multi-type CGP (MT-CGP)



MT-CGP

✤ Has *a big* function set

- Trying to incorporate *domain knowledge*
 - Easy to add new functions to help with a particular problem
- Functions deal with multiple data types
 - Functions are *overloaded*
 - Attempts are made at *human readable consistency*

Application 1: Digital circuit synthesis with CGP

- Digital Circuits with hundreds of variables can be optimized using CGP (Vassicek and Sekanina 2011)
 - Won the \$3000 silver award in human competitive workshop at GECCO 2011
- The method employs a SAT solver to identify whether two circuits are logically equivalent
 - In many cases this can be done in polynomial time











NOP	LOG	TRIANGLES
INP	MAX	LINES
INPP	MIN	SHIFTDOWN
SKIP	EQ	SHIFTUP
ADD	GAMMA	SHIFTLEFT
SUB	GAUSS	SHIFTRIGHT
CONST	SOBELX	SIFTa
MUL	SOBELY	GABOR
ADDC	AVG	NORMALIZE
SUBC	UNSHARPEN	RESCALE
MULC	THRESHOLD	GRABCUT
ABSDIFF	THRESHOLDBW	MINVALUE
CANNY	SMOOTHMEDIAN	MAXVALUE
DILATE	GOODFEATURESTOTRACK	AVGVALUE
ERODE	SQUARES	RESCALE
LAPLACE	CIRCLES	RESIZETHENGABOR



Evolve	d Filter code
SECCO-	<pre>public class MyEvolvedFilter : GpImageFilterRunner (public override GpImage RunFilter() { GpImage node0 ImputImages[0]; GpImage node1 = ImputImages[1]; GpImage node2 = node0.erode(1); GpImage node3 = node0.erode(1); GpImage node5 = node0.erode(1); GpImage node5 = node0.abcdiff(node2); GpImage node1 = node1.erode(3); GpImage node1 = node1.erode(3); GpImage node1 = node1.abcdiff(node3); GpImage node1 = node1.abcdiff(node3); GpImage node1 = node1.abu(node3); GpImage node1 = node1.abu(node3); GpImage node1 = node1.abu(node3); GpImage node5 = node5.qauss(15); GpImage node5 = node5.qauss(15); GpImage node5 = node50.qauss(15); GpImage node5 = node50.gauss(15); GpImage node5 = node50.gauss(15); GpImage node5 = node50.gauss(15); meturn node5 = node50.gaus</pre>
2013	}



Things we can do already:

- ♦Generate different filters for other objects.
 - Recently, allowing icub to detect its fingers (Leitner et al 2013)
- Find fast running filters.
- ✤Find them quickly.
- Show that filters are robust.
- Transfer code from offline learning to yarp module.
 - Software emits C# and C++ code
 - Running on Windows/Linux/Mac.

Tea-box filter: demonstration



Application 3: CGP encoded Artificial Neural Networks (CGPANN)

- CGP has been used to encode both feed-forward ANNs and recursive ANNs. The nodes genes consist of:
 - Connection genes (as usual)
 - Function genes (two)
 - Sigmoid, hyperbolic tangent
 - Weights
 - Each connection gene carries a real-numbered weight
- Pole balancing, Arm Throwing
 - Very competitive results with other TWEANN methods (*Khan, Khan and Miller 2010, Turner and Miller 2013*)
- Breast cancer detection (GECCO 2012, 2013 proceedings)

Cyclic CGP

2 13

- When outputs are allowed to connect to inputs through a clocked delay (flip-flop) it is possible to allow CGP to include feedback.
- By feeding back outputs generated by CGP to an input, it is possible to get CGP to generate sequences
 - In this way iteration is possible
- There are a couple of recent publications using recursion or iteration in CGP (*Khan, Khan and Miller 2010, Walker, Liu,*

Tempesti, Tyrrell 2010)





Conclusions

2013

- Cartesian Genetic Programming is a graph based GP method capable of representing many computational structures
 - programs, circuits, neural networks, systems of equations...
- Genetic encoding is compact, simple and easy to implement and can handle multiple outputs easily.
- The unique form of genetic redundancy in CGP makes mutational search highly effective
- The effectiveness of CGP has been compared with many other GP methods and it is very competitive

References



	Harding S. L., Miller J. F. Banzhaf W. Self Modifying Cartesian Genetic Programming: Finding algorithms that calculate pi and e to arbitrary precision, Proceedings of the Genetic and Evolutionary Computation Conference 2010
	Harding S. L., Miller J. F., Banzhaf W. A Survey of Self-Modifying CGP. Genetic Programming Theory and Practice, Riolo R., (Eds.). University of Michigan Illinois USA. Springer. 2010
	Harding S. L., Miller J. F. Banzhaf W. Self Modifying Cartesian Genetic Programming: Parity. Proceedings of Congress on Evolutionary Computation, IEEE Press (2009) 285-292
	Harding S. L., Miller J. F. Banzhaf W. Self Modifying Cartesian Genetic Programming: Fibonacci, Squares, Regression and Summing, Proceedings of the 10th European Conference on Genetic Programming, Springer LNCS (2009) 133-144
	Harding S. L., Miller J. F., Banzhaf W. Self-Modifying Cartesian Genetic Programming, Proceedings of Genetic and Evolutionary Computation Conference, ACM Press, (2007) 1021-1028.
	Harding S., Banzhaf W. Fast Genetic Programming on GPUs. Proceedings of 10th European Conference on Genetic Programming, Springer LNCS 4445 (2007) 90-101
	Harding S. L., Miller J. F. Evolution of Robot Controller Using Cartesian Proceedings of the 6th European Conference on Genetic Programming, Springer LNCS 3447 (2005) 62-72.
	Hirayama Y., Clarke T, Miller J. F. Fault Tolerant Control Using Cartesian Genetic Programming, Proceedings of Genetic and Evolutionary Computation Conference, ACM Press, (2008) 1523-1530.
	Kalganova T., Miller J. F., Evolving More Efficient Digital Circuits by Allowing Circuit Layout Evolution and Multi-Objective Fitness. Proceedings of the First NASA/DOD Workshop on Evolvable Hardware, IEEE Computer Society (1999) 54-63.
	Kalganova T., Miller J. F., Fogarty T. C. Some Aspects of an Evolvable Hardware Approach for Multiple- Valued Combinational Circuit Design Proceedings of the 2nd International Conference on Evolvable Systems: From Biology to Hardware. Springer LNCS 1478 (1998) 78-89.
	Kaufmann P., Platzner M. Advanced Techniques for the Creation and Propagation of Modules in Cartesian Genetic Programming. Proceedings of the Genetic and Evolutionary Computation Conference, ACM Press, (2008) 1219-1226.
	Kaufmann P., Platzner M. MOVES: A Modular Framework for Hardware Evolution. In Proceedings of the NASA/ESA Conference on Adaptive Hardware and Systems, IEEE Computer Society Press (2007) 2447-454
ieco	Kaufmann P. Platzner M. Toward Self-adaptive Embedded Systems: Multiobjective Hardware Evolution. In Proceedings of the 20th International Conference on Architecture of Computing Systems, Springer PICS 4415 (2007) 119-208.
	0 * 0

- Khan, G. M., Miller, J. F., Halliday, D. M. Evolution of Cartesian Genetic Programs for Development of Learning Neural Architecture, Evolutionary Computation, Vol. 19, No. 3 (2011) pp 469-523
- Khan, M. M., Khan, G. M., J. F. Miller, J. F. "Efficient representation of recurrent neural networks for markovian/non-markovian non-linear control problems," in Proceedings of the 10th International Conference on Intelligent Systems Design and Applications (ISDA2010) (2010) 615–620
- Khan, G. M., Miller J. F., Khan, M. M. Evolution of Optimal ANNs for Non-Linear Control Problems Using Cartesian Genetic Programming. Proceedings of International Conference on Artificial Intelligence (ICAI 2010)
- Khan, G. M., Halliday, D. M., Miller, J. F.,Intelligent agents capable of developing memory of their environment, Angelo Loula A., Queiroz, J. (Eds.) Advances in Modelling Adaptive and Cognitive Systems, Editora UEFS (2010)
- Khan G. M., Halliday D. M., Miller J. F. In Search of Intelligent Genes: The Cartesian Genetic Programming Neuron. Proceedings of Congress on Evolutionary Computation, IEEE Press (2009)
- Khan G. M., Halliday D. M., Miller J. F. Breaking the synaptic dogma: evolving a neuro-inspired developmental network. Proceedings of 7th International Conference on Simulated Evolution and Learning, LNCS, 5361 (2008) 11-20
- Khan G. M., Halliday D. M., Miller J. F. Coevolution of neuro-developmental programs that play checkers. Evolvable Systems: From Biology to Hardware. Springer LNCS 5216 (2008) 352 361.
- Khan G. M., Halliday D. M., Miller J. F. Coevolution of Intelligent Agents using Cartesian Genetic Programming. Proceedings of Genetic and Evolutionary Computation Conference, ACM Press, (2007) 269-276.
- Kuyucu T., Trefzer M. A., Miller J. F., Tyrrell A. M. On the Properties of Artificial Development and Its Use in Evolvable Hardware. Proceedings of Symposium on Artificial Life, Part of IEEE Symposium on Computational Intelligence, IEEE Press (2009).
- Liu H., Miller J. F., Tyrrell A. M., Intrinsic evolvable hardware implementation of a robust biological development model for digital systems, Proceedings of the NASA/DOD Evolvable Hardware Conference, IEEE Computer Society (2005) 87-92.
- Liu H., Miller J. F., Tyrrell A. M. A Biological Development Model for the Design of Robust Multiplier Applications of Evolutionary Computing: EvoHot 2005, Springer LNCS 3449 (2005) 195-204

Liu H, Miller J. F., Tyrrell A. M. An Intrinsic Robust Transient Fault-Tolerant Developmental Model for Digital Systems. Workshop on Regeneration and Learning in Developmental Systems, Genetic and Bolting Computation Conference (2004).

Sekanina, L. Evolvable Components - From Theory to Hardware Implementations, Springer (2003) Sekanina, L. Image Filter Design with Evolvable Hardware, Proceedings of Evolutionary Image Analysis and Signal Processing, Springer LNCS 2279 (2002) 255-266.

- Sekanina, L. Vašiček Z. On the Practical Limits of the Evolutionary Digital Filter Design at the Gate Level. Proceedings of EvoHOT, Springer, LNCS 3907 (2006) 344-355
- Miller J. F. Cartesian Genetic Programming, Springer 2011.

- Miller J.F., Smith S.L. Redundancy and Computational Efficiency in Cartesian Genetic Programming, IEEE Transactions on Evolutionary Computation, 10 (2006) 167-174.
- Miller J. F. Evolving a self-repairing, self-regulating, French flag organism. Proceedings of Genetic and Evolutionary Computation Conference, Springer LNCS 3102 (2004) 129-139.
- Miller J. F., Thomson P. Beyond the Complexity Ceiling: Evolution, Emergence and Regeneration. Workshop on Regeneration and Learning in Developmental Systems, Genetic and Evolutionary Computation Conference (2004)
- Miller J.F., Banzhaf W., Evolving the Program for a Cell From French Flags to Boolean Circuits. Kumar S., Bentley P. On Growth, Form and Computers. Elsevier Academic Press (2003).
- Miller J. F., Thomson P. A Developmental Method for Growing Graphs and Circuits. Proceedings of the 5th International Conference on Evolvable Systems: From Biology to Hardware, Springer LNCS 2606 (2003) 93-104
- Miller J. F. Evolving developmental programs for adaptation, morphogenesis, and self-repair. Proceedings of the 7th European Conference on Artificial Life, Springer LNAI 2801 (2003) 256-265.
- Miller J. F. What bloat? Cartesian Genetic Programming on Boolean problems. Genetic and Evolutionary Computation Conference, Late breaking paper (2001) 295 302.
- Miller J. F., Hartmann M. Evolving messy gates for fault tolerance: some preliminary findings. Proceedings of the 3rd NASA/DOD Workshop on Evolvable Hardware. IEEE Computer Society (2001) 116-123.
- Miller J. F., Hartmann M. Untidy evolution: Evolving messy gates for fault tolerance. Proceedings of the 4th International Conference on Evolvable Systems: From Biology to Hardware. Springer LNCS 2210 (2001) 14-25.
- Miller J.F., Kalganova T., Lipnitskaya N., Job D. The Genetic Algorithm as a Discovery Engine: Strange Circuits and New Principles. Creative Evolutionary Systems. Morgan Kaufmann (2001).

- Miller J.F., Job D., Vassilev V.K. Principles in the Evolutionary Design of Digital Circuits Part I. Journal of Genetic Programming and Evolvable Machines, 1 (2000) 8-35. Miller J.F., Job D., Vassilev V.K. Principles in the Evolutionary Design of Digital Circuits - Part II. Journal of Genetic Programming and Evolvable Machines, 3 (2000) 259-288.
- Miller J. F., Thomson P. Cartesian Genetic Programming. Proceedings of the 3rd European Conference on Genetic Programming. Springer LNCS 1802 (2000) 121-132.
- Miller J. F. On the filtering properties of evolved gate arrays. Proceedings of the First NASA/DOD Workshop on Evolvable Hardware. IEEE Computer Society (1999) 2-11. Miller J. F. Digital Filter Design at Gate-level using Evolutionary Algorithms. Proceedings of the 1st Genetic
- and Evolutionary Computation Conference. Morgan Kaufmann (1999) 1127-1134. Miller J. F. An empirical study of the efficiency of learning boolean functions using a Cartesian Genetic
- Programming Approach. Proceedings of the 1st Genetic and Evolutionary Computation Conference. Morgan Kaufmann (1999) 1135-1142.
- Miller J. F. Evolution of Digital Filters using a Gate Array Model. Proceedings of the First Workshop on Image Analysis and Signal Processing. Springer LNCS 1596 (1999) 17-30.
- Miller J. F., Kalganova T., Lipnitskaya N., Job D. The Genetic Algorithm as a Discovery Engine: Strange Circuits and New Principles. Proceedings of the workshop on the AISB Sympos Evolutionary Systems. AISB (1999) 65-74.
- Miller J. F., Thomson P. Aspects of Digital Evolution: Evolvability and Architecture. Proceedings of The Fifth International Conference on Parallel Problem Solving from Nature. Springer LNCS 1498 (1998) 927-936
- Miller J. F., Thomson P. Aspects of Digital Evolution: Geometry and Learning. Proceedings of the 2nd International Conference on Evolvable Systems: From Biology to Hardware. Springer LNCS 1478 (1998) 25-25
- Miller J. F., Thomson P. Evolving Digital Electronic Circuits for Real-Valued Function Generation using a Genetic Algorithm . Proceedings of the 3rd Conference on Genetic Programming. Morgan Kaufmann (1998) 863-868.
- Miller J.F., Thomson P., Fogarty T.C. Designing Electronic Circuits Using Evolutionary Algorithms: Arithmetic Circuits: A Case Study. Genetic Algorithms and Evolution Strategies in Engineering and Computer Science: Recent Advancements and Industrial Applications. Quagharella, D., Periaux J. Poloni C., Winter G. (Eds.). Wiley (1997)
- Parte A. J., Stepney, S., Representation and Structural biases in CGP, Proceedings of Congress on Dolutionary Computation, IEEE Press (2009)

Rothermich J., Wang F., Miller J. F. Adaptivity in Cell Based Optimization for Information Ecosystems. Proceedings of the Congress on Evolutionary Computation. IEEE Press (2003) 490-497.

- Rothermich J., Miller J. F. Studying the Emergence of Multicellularity with Cartesian Genetic Programming in Artificial Life. Proceedings of the 2002 U.K. Workshop on Computational Intelligence (2002).
- Seaton, T, Miller, J. F., Clarke, T. Semantic Bias in Program Coevolution. Proceedings of the European Conference on Genetic Programming, (Krawiec, K et al. (Eds.) pp. 193-204, Springer, LNCS Vol. 7831 2013
- Seaton, T, Miller, J. F., Clarke, T. An Ecological Approach to Measuring Locality in Linear Genotype to Phenotype Maps. Proceedings of the European Conference on Genetic Programming, pp. 170-181, Springer, LNCS Vol. 7244 2012.
- Seaton, T., Brown G., Miller J. F., Analytic Solutions to Differential Equations under Graph-based Genetic Programming. Proceedings of the 13th European Conference on Genetic Programming. Springer LNCS 6021 (2010) 232-243
- Kisung Seo, K., Hyun, S. Toward Automatic Gait Generation for Quadruped Robots Using Cartesian Genetic Programming. EvoApplications 2013, LNCS Vol. 7835, pp. 599–605.
- Vašiček Z, Sekanina L. Hardware Accelerators for Cartesian Genetic Programming, Proc. Eleventh European Conference on Genetic Programming, Springer LNCS Vol. 4971 (2008) 230-241
- Vašiček, Z. Sekanina, L.. Formal verification of candidate solutions for post-synthesis evolutionary optimization in evolvable hardware. Genetic Programming and Evolvable Machines, 12(3) (2011) 305-327, 2011.
- Vassilev V. K., Miller J. F. Scalability Problems of Digital Circuit Evolution. Proceedings of the 2nd NASA/DOD Workshop on Evolvable Hardware. IEEE Computer Society (2000) 55-64.
- Vassilev V. K., Miller J. F. The Advantages of Landscape Neutrality in Digital Circuit Evolution. Proceedings of the 3rd International Conference on Evolvable Systems: From Biology to Hardware. Springer LNCS 1801 (2000) 252-263.
- Vassilev V. K., Miller J. F. Towards the Automatic Design of More Efficient Digital Circuits. Proceedings of the 2nd NASA/DOD Workshop on Evolvable Hardware. IEEE Computer Society (2000) 151-160. Vassilev V. K., Miller J. F., Fogarty T. C. Digital Circuit Evolution and Fitness Landscapes, Proceedings of the
- Congress on Evolutionary Computation. IEEE Press (1999) 1299-1306. Vassiley V. K., Miller J. F., Fogarty T. C. On the Nature of Two-Bit Multiplier Landscapes. Proceedings of the
- First NASA/DOD Workshop on Evolvable Hardware. IEEE Computer Society (1999) 36-45. 2013

Walker J. A., Miller J. F. Embedded Cartesian Genetic Programming and the Lawnmower and Hierarchical-if-and-only-if Problems, Proceedings of the 2006 Genetic and Evolutionary Computation Conference. ACM Press, (2006) 911-918.

- Walker J. A., Miller J. F. Improving the Evolvability of Digital Multipliers Using Embedded Cartesian Genetic Programming and Product Reduction. Proceedings of 6th International Conference in Evolvable Systems. Springer, LNCS 3637 (2005) 131-142.
- Walker J. A., Miller J. F. Investigating the performance of module acquisition in Cartesian Genetic Programming, Proceedings of the 2005 conference on Genetic and Evolutionary Computation. ACM Press (2005) 1649-1656.
- Walker J. A., Miller J. F. Evolution and Acquisition of Modules in Cartesian Genetic Programming. Proceedings of the 7th European Conference on Genetic Programming. Springer LNCS 3003 (2004) 187-197.
 Yu T., Miller J.F., Through the Interaction of Neutral and Adaptive Mutations Evolutionary Search Finds a Way. *Artificial Life*, 12 (2006) 525-551.
- ., Miller J. F. Finding Needles in Haystacks Is Not Hard with Neutrality. Proceedings of the 5th European Conference on Genetic Programming. Springer LNCS 2278 (2002) 13-25. Yu T
- Yu T., Miller J. F. Neutrality and Evolvability of a Boolean Function Landscape, Proceedings of the 4th European Conference on Genetic Programming. Springer LNCS, 2038, (2001) 204-217.
- Zhan S., J.F. Miller, A. M., Tyrrell. An evolutionary system using development and artificial Genetic Regulatory Networks for electronic circuit design, Biosystems, 96 (3) (2009) pp 176-192
- Zhan S., Miller J. F., Tyrrell A. M. Obtaining System Robustness by Minicking Natural Mechanisms Proceedings of Congress on Evolutionary Computation. IEEE Press (2009) Zhan S., Miller J. F., Tyrrell A. M. A Development Gene Regulation Network For Constructing Electronic
- Circuits . Evolvable Systems: From Biology to Hardware. LNCS 5216 (2008) 177 188 Zhan S., Miller J. F., Tyrrell A. M. An Evolutionary System using Development and Artificial Genetic
- Regulatory Networks Proceedings of 9th IEEE World Congress on Computational Intelligence. Congress on Evolutionary Computation. IEEE Press (2008) 815-822.



Voss M. S. Social programming using functional swarm optimization. In Proceedings of IEEE Swarm Intelligence Symposium (2003) Voss M. S., Howland, J. C. III Financial modelling using social programming. Financial Engineering and Applications (2003) Völk K., Miller J. F., Smith, S. L. Multiple Networks CGP for the Classification of Mammograms. Proceedings of the 11th European Workshop on Image Analysis and Signal Processing (EvoIASP), Springer LNCS (2009) Walker J. A., Liu Y., Tempesti G., Tyrrell A. M., "Automatic Code Generation on a MOVE Processor Using Cartesian Genetic Programming," in Proceedings of the International Conference on Evolvable Systems: From Biology to Hardware, Springer LNCS vol. 6274 (2010) 238–249 Walker J.A., Völk, K., Smith, S. L., Miller, J. F. Parallel evolution using multi-chromosome cartesian genetic programming, Genetic Programming and Evolvable Machines, 10 (4), (2009) pp 417-445 Walker J. A., Hilder, J. A., Tyrrell, A. M. Towards Evolving Industry-feasible Intrinsic Variability Tolerant CMOS Designs, Proceedings of Congress on Evolutionary Computation, IEEE Press (2009) Walker J.A., Miller J.F. The Automatic Acquisition, Evolution and Reuse of Modules in Cartesian Genetic Programming. IEEE Transactions on Evolutionary Computation, 12 (2008) 397-417. Walker J. A. Modular Cartesian Genetic Programming. PhD thesis, University of York, 2008. Walker J. A., Miller J. F. Solving Real-valued Optimisation Problems using Cartesian Genetic Programming. Proceedings of Genetic and Evolutionary Computation Conference, ACM Press (2007) 1724-1730. Walker J A., Miller J. F. Changing the Genospace: Solving GA Problems using Cartesian Genetic Programming, Proceedings of 10th European Conference on Genetic Programming, Springer LNCS 4445 (2007) 261-270. Walker J. A., Miller J. F. Predicting Prime Numbers using Cartesian Genetic Programming, Proceedings of 10th European Conference on Genetic Programming, Springer LNCS 4445, (2007) 205-216 Walker J. A., Miller J. F., Cavill R. A Multi-chromosome Approach to Standard and Embedded Cartesian Genetic Programming, Proceedings of the 2006 Genetic and Evolutionary Computation Conference. ACM Press, (2006) 903-910.